EUROPEAN PATENT OFFICE
U.S. PATENT AND TRADEMARK OFFICE

CPC NOTICE OF CHANGES 1714

DATE: MAY 1, 2025

PROJECT MP12387

The following classification changes will be effected by this Notice of Changes:

Action	Subclass	Group(s)
SCHEME:		
Titles Changed:	H03L	1/027, 7/14
DEFINITIONS:		
Definitions Modified:	H03L	7/0807, 7/14, 7/185, 7/187, 7/24, 7/26

No other subclasses/groups are impacted by this Notice of Changes.

This Notice of Changes includes the following *[Check the ones included]:*

1. CLASSIFICATION SCHEME CHANGES

    ☒   A. New, Modified or Deleted Group(s)

    ☐   B. New, Modified or Deleted Warning(s)

    ☐   C. New, Modified or Deleted Note(s)

    ☐   D. New, Modified or Deleted Guidance Heading(s)

2. DEFINITIONS

    ☒   A. New or Modified Definitions (Full definition template)

    ☐   B. Modified or Deleted Definitions (Definitions Quick Fix)

3. ☐   REVISION CONCORDANCE LIST (RCL)

4. ☐   CHANGES TO THE CPC-TO-IPC CONCORDANCE LIST (CICL)

5. ☐   CHANGES TO THE CROSS-REFERENCE LIST (CRL)

1. CLASSIFICATION SCHEME CHANGES

   A. New, Modified or Deleted Group(s)

SUBCLASS H03L - AUTOMATIC CONTROL, STARTING, SYNCHRONISATION

Type*	Symbol	Indent Level Number of dots (e.g. 0, 1, 2)	Title "CPC only" text should normally be enclosed in {curly brackets}**	Transferred to[#]
M	H03L 1/027	3	{by using frequency conversion means which is variable with temperature, e.g. mixer, frequency divider, pulse add/subtract logic circuit (H03L 1/023, H03L 1/026 take precedence)}	
M	H03L 7/14	3	for assuring constant frequency when supply or correction voltages fail	

*N = new entries where reclassification into entries is involved; C = entries with modified file scope where reclassification of documents from the entries is involved; Q = new entries which are firstly populated with documents via administrative transfers from deleted (D) entries. Afterwards, the transferred documents into the Q entry will either stay or be moved to more appropriate entries, as determined by intellectual reclassification; T= existing entries with enlarged file scope, which receive documents from C or D entries, e.g. when a limiting reference is removed from the entry title; M = entries with no change to the file scope (no reclassification); D = deleted entries; F = frozen entries will be deleted once reclassification of documents from the entries is completed; U = entries that are unchanged.

NOTES:
- **No {curly brackets} are used for titles in CPC only subclasses, e.g. C12Y, A23Y; 2000 series symbol titles of groups found at the end of schemes (orthogonal codes); or the Y section titles. The {curly brackets} are used for 2000 series symbol titles found interspersed throughout the main trunk schemes (breakdown codes).
- U groups: it is obligatory to display the required "anchor" symbol (U group), i.e. the entry immediately preceding a new group or an array of new groups to be created (in case new groups are not clearly subgroups of C-type groups). Always include the symbol, indent level and title of the U group in the table above.
- All entry types should be included in the scheme changes table above for better understanding of the overall scheme change picture. Symbol, indent level, and title are required for all types.
- "Transferred to" column must be completed for all C, D, F, and Q type entries. F groups will be deleted once reclassification is completed.
- When multiple symbols are included in the "Transferred to" column, avoid using ranges of symbols in order to be as precise as possible.
- For administrative transfer of documents, the following text should be used: "<administrative transfer to XX>", "<administrative transfer to XX and YY simultaneously>", or "<administrative transfer to XX, YY, ...and ZZ simultaneously>" when administrative transfer of the same documents is to more than one place.
- Administrative transfer to main trunk groups is assumed to be the source allocation type, unless otherwise indicated.
- Administrative transfer to 2000/Y series groups is assumed to be "additional information".
- If needed, instructions for allocation type should be indicated within the angle brackets using the abbreviations "ADD" or "INV": <administrative transfer to XX ADD> , <administrative transfer to XX INV>, or < administrative transfer to XX ADD, YY INV, ... and ZZ ADD simultaneously>.
- In certain situations, the "D" entries of 2000-series or Y-series groups may not require a destination ("Transferred to") symbol, however it is required to specify "<no transfer>" in the "Transferred to" column for such cases.
- For finalisation projects, the deleted "F" symbols should have <no transfer> in the "Transferred to" column.

For more details about the types of scheme change, see CPC Guide.

2. A. DEFINITIONS (Modified)

H03L 7/0807

<u>Replace</u>: The existing Synonyms and Keywords table with the following updated table.

Synonyms and Keywords

In patent documents the following abbreviations are often used:

DCR	data/clock recovery
CDR	clock/data recovery

H03L 7/14

<u>Replace</u>: The existing Definition statement text with the following updated text.

Definition statement

This place covers:

Arrangements and methods for assuring constant output frequency of the generator when the reference signal fails or is interrupted, or when the control signal of the controlled oscillator of the PLL or the controlled delay of the DLL fails or is interrupted.

Automatic control for assuring constant frequency when input or correction signals are interrupted.

H03L 7/185

References

Replace: The existing Limiting references table with the following updated table.

Limiting references

This place does not cover:

Indirect frequency synthesis using means for coarse tuning the voltage-controlled oscillator of the loop	H03L 7/187
Indirect frequency synthesis using means for coarse tuning the voltage-controlled oscillator of the loop comprising a D/A converter for generating a coarse tuning voltage	H03L 7/189
Indirect frequency synthesis using at least two different signals from the frequency divider or the counter for determining the time difference	H03L 7/191
Indirect frequency synthesis using a frequency divider/counter comprising a commutable pre-divider, e.g. a two modulus divider	H03L 7/193
Indirect frequency synthesis using a frequency divider or counter in the loop in which the counter of the loop counts between two different non zero numbers, e.g. for generating an offset frequency	H03L 7/195

Insert: The following new Informative references section.

Informative references

Attention is drawn to the following places, which may be of interest for search:

Mixer without a frequency divider or counter	H03L 2207/12

H03L 7/187

References

Insert: The following new Limiting references section.

Limiting references

This place does not cover:

Indirect frequency synthesis using at least two different signals from the frequency divider or the counter for determining the time difference	H03L 7/191
Indirect frequency synthesis using a frequency divider/counter comprising a commutable pre-divider, e.g. a two modulus divider	H03L 7/193
Indirect frequency synthesis using a frequency divider or counter in the loop in which the counter of the loop counts between two different non zero numbers, e.g. for generating an offset frequency	H03L 7/195

Replace: The existing Special rules text with the following updated text.

Special rules of classification

When the means for coarse tuning the controlled oscillator is not specific for frequency synthesizers, the document would be (additionally) classified in the subgroup H03L 7/10.

H03L 7/24

Replace: The existing Definition statement text with the following updated text.

Definition statement

This place covers:

Also, injection-locked oscillators, oscillator arrays, mutually-coupled oscillators.

H03L 7/26

Replace: The existing Definition statement text with the following updated text.

Definition statement

This place covers:

Also, atomic clocks.